

(19)



Europäisches Patentamt  
European Patent Office  
Office européen des brevets



(11) Publication number:

**0 324 308 A3**

(12)

**EUROPEAN PATENT APPLICATION**

(21) Application number: 88480070.7

(51) Int. Cl.<sup>5</sup>: G06F 9/30

(22) Date of filing: 08.11.88

(30) Priority: 11.01.88 US 142585

(43) Date of publication of application:  
19.07.89 Bulletin 89/29(84) Designated Contracting States:  
DE FR GB(88) Date of deferred publication of the search report:  
27.03.91 Bulletin 91/13

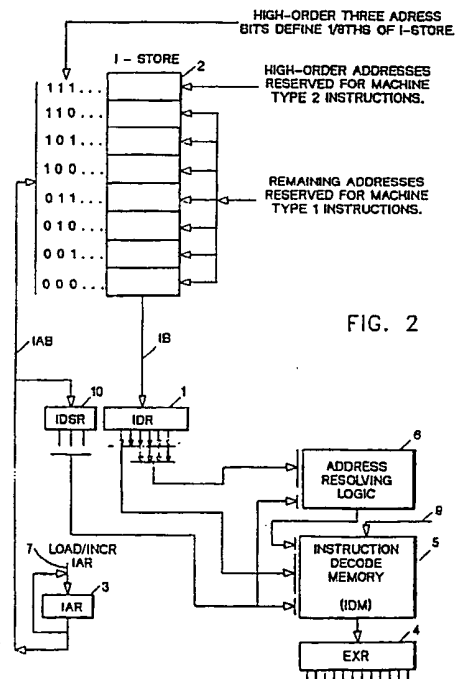
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(54) Method of decoding plural incompatible format instructions.

(57) This invention relates to the architectural design of a data processing system and its method of operation which permits normally incompatible plural format instructions for dissimilar processors to be placed intermixed in the instruction storage of a single machine but to be accurately decoded and executed properly regardless. Instructions in different formats which are normally incompatible, are placed in predefined or segregated areas of the instruction store (2). Instructions are fetched and decoded in an instruction decode memory (5) in a manner which uses portions of both the fetched-from address in the instruction (2) store and the instruction itself. Decoding is thus determined in part by where in the instruction store the instruction resided when fetched, and by the specific instruction itself. This approach is compatible with both ordinary processor architecture and with pipelined processor architectures.



EP 0 324 308 A3



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## EUROPEAN SEARCH REPORT

Application Number

EP 88 48 0070

DOCUMENTS CONSIDERED TO BE RELEVANT																	
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)														
A	EP-A-0 169 565 (NEC CORP.) * Abstract; page 17, line 19 - page 18, line 16 * - - - -		G 06 F 9/30														
A	IBM TECHNICAL DISCLOSURE BULLETIN, vol. 22, no. 4, September 1979, pages 1396-1397, New York, US; K.A. DAVIDSON: "Instruction decoding" * Whole article * - - - -																
A	IBM TECHNICAL DISCLOSURE BULLETIN, vol. 29, no. 2, July 1986, pages 548-549, New York, US; "Full function series'1 instruction set emulator" * Whole article * - - - -																
A	PATENT ABSTRACTS OF JAPAN, vol. 7, no. 75 (P-187)[1220], 29th March 1983; & JP-A-58 5845 (NIPPON DENKI K.K.) 13-01-1983 * Whole abstract * - - - -																
A	PATENT ABSTRACTS OF JAPAN, vol. 6, no. 30 (P-103)[908], 23rd February 1982; & JP-A-56 149 645 (NIPPON DENKI K.K.) 19-11-1981 * Whole abstract * - - - -																
A	US-A-4 575 797 (GRUNER et al.) * Abstract * - - - - -		G 06 F 9/30 G 06 F 9/44														
The present search report has been drawn up for all claims																	
Place of search The Hague		Date of completion of search 12 December 90	Examiner QUESSON C.J.														
<table border="0"><tr><td>CATEGORY OF CITED DOCUMENTS</td><td>E : earlier patent document, but published on, or after the filing date</td></tr><tr><td>X : particularly relevant if taken alone</td><td>D : document cited in the application</td></tr><tr><td>Y : particularly relevant if combined with another document of the same category</td><td>L : document cited for other reasons</td></tr><tr><td>A : technological background</td><td></td></tr><tr><td>O : non-written disclosure</td><td>&amp; : member of the same patent family, corresponding document</td></tr><tr><td>P : intermediate document</td><td></td></tr><tr><td>T : theory or principle underlying the invention</td><td></td></tr></table>				CATEGORY OF CITED DOCUMENTS	E : earlier patent document, but published on, or after the filing date	X : particularly relevant if taken alone	D : document cited in the application	Y : particularly relevant if combined with another document of the same category	L : document cited for other reasons	A : technological background		O : non-written disclosure	& : member of the same patent family, corresponding document	P : intermediate document		T : theory or principle underlying the invention	
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